

4 constructing at least two motion vectors, the first motion vector  
5 being constructed from a present video frame and a video frame prior to the  
6 present video frame, the second motion vector being constructed from the  
7 present video frame and a further video frame at least two frames prior to the  
8 present video frame;

9 compensating the motion of at least two previously decoded  
10 video frames stored in a memory with respect to a corresponding one of the at  
11 least two motion vectors;

12 generating a predicted video frame from each of the at least two  
13 previously decoded video frames after receiving compensating for  
14 reconstructing the pixel block of the present video frame,

15 wherein the predicted video frame is selected depending on the  
16 presence or absence of a decoding error contained in said predicted video  
17 frame.

1 2. (Twice Amended) A method of decoding a moving image  
2 signal of claim 1, wherein if plural predicted video frames are free from  
3 decoding error.

4 the predicted video frame produced from the latest decoded  
5 frame in time out of said predicted video frames free from decoding error is  
6 used in reconstruction of the present processing pixel block.

5 constructing at least two motion vectors, the first motion vector  
6 being constructed from a present video frame and a video frame prior to the  
7 present video frame, the second motion vector being constructed from the  
8 present video frame and a further video frame at least two frames prior to the  
9 present video frame

10 inter-coding the present processing pixel block when the  
11 correlation of the at least two video frames in memory when compensated of  
12 motion by said at least two motion vectors is greater than a predetermined  
13 value, and

14 intra-coding the present processing pixel block when the  
15 correlation of the at least two video images in memory when compensated of  
16 motion by said at least two vectors is less than a predetermined value.

1                   4. (Twice Amended) A method of coding a moving image signal,  
2 the image signal being a stream of pixel blocks segregated into video frames,  
3 for constructing at least two motion vectors, comprising the steps of:

4 storing at least two decoded video frames in a memory;

10                   selecting a predicted video frame produced from the latest  
11   decoded video frame in time out of at least two video frames images in -  
12   memory when compensated of motion by said at least two motion vectors; and  
13                   coding the present processing pixel block in accordance with the  
14   selected predicted video frame.

1                   5. (Twice Amended) A moving image signal decoding apparatus  
2   comprising:

3                   variable length code decoding means for decoding at least two  
4   motion vectors relating to the present processing pixel block, the first motion  
5   vector being constructed from a present video frame and a frame prior to the  
6   present video frame, the second motion vector being constructed from the  
7   present video frame and a further video frame at least two frames prior to the  
8   present video frame,

9                   motion compensation means for compensating the motion of a  
10   previously coded video frame with respect to each one of said at least two  
11   motion vectors, and generating at least two predicted video frames relating to  
12   the present processing pixel block,

13                   bit error detecting means for detecting a bit error from the output  
14   of said variable length code decoding means,

15                   memory means for storing the bit error of said bit error detecting  
16   means, and

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17 predicted video frame selecting means for recognizing the  
18 presence or absence of decoding error contained in said at least two predicted  
19 video frames, and selecting the predicted video frame to be used in  
20 reconstruction of the present processing pixel block.

1 *sub 6.2* 7. (Twice Amended) A moving image signal decoding apparatus  
2 of claim 5, wherein the memory means stores the bit errors of plural video  
3 frames by plotting the pixel blocks in which bit error is detected in each video  
4 frame in a map form

1 8. (Twice Amended) A moving image signal decoding apparatus  
2 of claim 7, wherein the memory means comprises plural decoding error map  
3 memories storing each video frame consecutive in time, and changeover means,  
4 said plural decoding error map memories being changed over by said  
5 changeover means, and issued.

1 9. (Twice Amended) A moving image signal coding apparatus  
2 comprising:

3 motion vector detecting means for constructing at least two  
4 motion vectors, the first motion vector being constructed from a present frame  
5 and a frame processed immediately prior to the present frame, the second  
6 motion vector being constructed from the present frame and the frame  
7 processed two frames immediately prior to the present frame for relating to the  
8 present processing pixel block.

9 motion compensation means for issuing plural predicted video  
10 frames based on stored video frames from the output of said motion vector  
11 detecting means, and

12 intra/inter judging means for inter-coding the present processing  
13 pixel block when the correlation of at least two predicted video frames  
14 compensated of motion by said at least two motion vectors as the output of said  
15 motion compensation means is greater than a predetermined value, and intra-  
16 coding the present processing pixel block when the correlation of said at least  
17 two predicted video frames is less than a predetermined value.

1 10. (Twice Amended) A moving image signal coding apparatus  
2 of claim 9, further comprising:

3 predicted image combining means for combining the at least two  
4 predicted video frames compensated by said at least two motion vectors, and

5 prediction error calculating means for calculating the prediction  
6 error from the output of said predicted video frame combining means and a  
7 macro block of the present video frame,

8 wherein the intra/inter judging means judges before processing  
9 by comparing the variance of present processing pixel block and the variance  
10 of prediction error from the output of the prediction error calculating means to  
11 judge processing before intra/inter coding, and processes next intra/inter  
12 judgement only when judged to be inter-coding.

11. (Twice Amended) A moving image signal coding apparatus  
of claim 10, wherein said predicted image combining means issues a predicted  
video frame produced from the latest decoded video frame in time out of the at  
least two or more predicted video frames compensated of motion by said at  
least two motion vectors for use in coding of the present processing pixel  
block.